

#15/amdt C
6-3-03
SRL



PATENT
Attorney Docket No.: SAM-0164

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Myeong-cheol Kim, et al.
Serial No.: 09/731,385
Filing Date: December 6, 2000
Title: SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT AND FABRICATING METHOD THEREFOR

Examiner: Nadav, O.
Group Art Unit: 2811

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

5-27-03

Date

Amy Green

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT A

Sir:

This is in response to the Office Action mailed on February 27, 2003.

Please amend the application as follows:

In the Claims

Please amend the claims as follows:

RECEIVED
MAY 30 2003
TECHNOLOGY CENTER 2800

1. (Three Times Amended) A semiconductor device having a self-aligned contact, the semiconductor device comprising:
- a plurality of conductive patterns formed to be adjacent to one another by sequentially stacking and patterning a first conductive layer and a mask layer on a particular underlying layer;
 - a first insulation layer filling a gap between adjacent conductive patterns, the first insulation layer being formed of a first insulating material;

C/C
cont
Sub
DI